

#### Sequential instructions / Step ladder instructions (SFC) / Function instructions

#### ■ Sequential instructions

Instruction	Operand	Ladder symbol	Function
ORG		<b>⊢</b>	Network starts by an A contact
ORG NOT	X,Y,M,	<del></del>	Network starts by a B contact
ORG TU	S,T,C	<b>→</b>  ↑  <b>→</b>	Network starts by a TU contact
ORG TD		$\longrightarrow$	Network starts by a TD contact
ORG OPEN		•	Network starts by an open contact
ORG SHORT		•	Network starts by a short contact
LD		<b>⊢</b> ⊢•	Branch line starts by an A contact
LD NOT	X,Y,M,	<b>├</b> ┤/ <b>├</b>	Branch line starts by a B contact
LD TU	S,T,C	<b>├</b> ┤├ <b>-</b>	Branch line starts by a TU contact
LD TD		<b>├</b> ┤↓├ <b>-•</b>	Branch line starts by a TD contact
LD OPEN		+ •	Branch line starts by an open contact
LD SHORT		+	Branch line starts by a short contact
AND		<b>→</b>	Serial connect with an A contact
AND NOT	X,Y,M,	<b>→</b>  / -•	Serial connect with a B contact
AND TU	S,T,C	<b>→</b>   ↑   <b>→</b>	Serial connect with a TU contact
AND TD		<b>→</b>   ↓   <b>→</b>	Serial connect with a TD contact
AND OPEN		<b>→</b>	Serial connect with an open contact
AND SHORT			Serial connect with a short contact

Instruction	Operand	Ladder symbol	Function
OR		<b>+</b>   <b>+</b>	Parallel connect with an A contact
OR NOT	X,Y,M,	1/ -1	Parallel connect with a B contact
OR TU	S,T,C	<b>1</b> ↑ ↑ ↑ ↑	Parallel connect with a TU contact
OR TD		<b>1</b> —↓	Parallel connect with a TD contact
OR OPEN		1 1	Parallel connect with an open contact
OR SHORT		<b></b>	Parallel connect with a short contact
ANDLD			Concatenate two blocks in series
ORLD			Merge two blocks in parallel
OUT	VM 0	<b>→</b> ( )	Output result to coil
OUT NOT	Y,M,S	<b>→</b> (/)	Output the inverse of result to a coil
OUT L	Υ	<b>→</b> (L)	Output result to a retentive coil
OUT	TR		Store node status in temporary relay
LD	IK		Retrieve node status from temporary relay
TU		<b>-</b> —↑	Take differential up of node status to node status
TD	<b>←</b> ↓ <b>→</b>		Take differential down of node status to node status
NOT		<b>-</b> /	Inverse node status
SET		<b>→</b> (S)	Set a coil
RST	<b>-</b> ● (R)		Reset a coil

## ■ Step ladder instructions (SFC)

Instruction	Operand	Ladder symbol	Function	
STP	Snnn	STP	Define STEP program	
STPEND		STPEND	STEP program end	

Instruction	Operand	Ladder symbol	Function
то	Snnn		STEP divergence
FROM		FROM	STEP convergence

#### **■ Function instructions**

Category	NO.	Instruction	Derivative	Function
Timer		Tnnn		General timer instruction (T0 ~ T255)
Counter		Cnnn	General counter instruction (C0 ~ C255)	
		SET	DP	Set all bits of register or a discrete point to 1
Setting / Resetting		RST	DP	Clear all bits of register or a discrete point to 0
reconting	114	Z-WR	Р	Zone set or clear
	4	DIFU		Take differential up of the node status to operand
Digital operation	5	DIFD		Take differential down of the node status to operand
	10	TOGG		Toggle the coil status
	11	(+)	DP	$Sa+Sb \rightarrow D$
	12	(-)	DP	$Sa-Sb \rightarrow D$
	13	(×)	DP	$Sa \times Sb \rightarrow D$
	14	(/)	DP	$Sa / Sb \rightarrow D$
	15	(+1)	DP	Add 1 to D
	16	(-1)	DP	Subtract 1 from D
Mathematical	23	DIV48	Р	48 bits integer division Sa / Sb $\rightarrow$ D
operation	24	SUM	DP	Sum of N consecutive values
	25	MEAN	DP	Average of N consecutive values
	26 SQRT		DP	Square root of S
	27	NEG	DP	Two's complement of D (Negative number)
	28	ABS	DP	Absolute value of D
	29	EXT	Р	Extend 16 bits into 32 bits
	30	PID	Р	PID calculation

Category	NO.	Instruction	Derivative	Function
	31	CRC16	Р	CRC16 calculation
	32	ADCNV		Offset and full scale conversion for analog I/O
	200	l→F	DP	Integer to floating point number conversion
	201	F→I	DP	Floating point number to integer conversion
	202	FADD	P	Addition of floating point number
	203	FSUB	P	Subtraction of floating point number
	204	FMUL	Р	Multiplication of floating point number
Mathematical	205	FDIV	Р	Division of floating point number
operation	206	FCMP	Р	Comparison of floating point number
	207	FZCP	Р	Zone comparison of floating point number
	208	FSQR	Р	Square root of floating point number
	209	FSIN	Р	SIN trigonometric function
	210	FCOS	Р	COS trigonometric function
	211	FTAN	Р	TAN trigonometric function
	212	FNEG	Р	Change sign of floating point number
	213	FABS	Р	Absolute value of floating point number
	18	AND	DP	Sa AND Sb
Logic	19	OR	DP	Sa OR Sb
operation	35	XOR	DP	Sa XOR Sb
	36	XNR	DP	Sa XNR Sb
	17	CMP	DP	Value Compare
Comparison	37	ZNCMP	DP	Zone Compare

#### **Function instructions**

#### (Continues)

Category	NO.	Instruction	Derivative	Function		
	8	MOV	DP	Move S to D		
	9	MOV/	DP	Inverse S and move to D		
	40	BITRD	DP	Move the Bit-N of S to FO		
	41	BITWR	DP	Write INB input to the Bit-N of D		
	42	BITMV	DP	Move the Bit-Ns of S to the Bit -Nd of D		
	43	NBMV	DP	Move the Nibble-Ns of S to the Nibble-Nd of D		
Move	44	BYMV	DP	Move the Byte-Ns of S to the Byte-Nd of D		
operation	45	XCHG	DP	Exchange Da and Db		
	46	SWAP	Р	Swap the High-Byte of D with the Low-Byte of D		
	47	UNIT	Р	Take Nb0 of N words to form a Word		
	48	DIST	Р	Distribute N Nb of S to Nb0 of N Words		
	49	BUNIT	Р	Low byte of words re-unit		
	50	BDIST	Р	Words split into multi-byte		
	160	RW-FR	DP	File register access		
	6	BSHF	DP	Shift D right 1 bit or left 1 bit		
	51	SHFL	DP	Shift D left N bits		
Shift / Rotation	52	SHFR	DP	Shift D right N bits		
	53	ROTL	DP	Rotate D left N bits		
	54	ROTR	DP	Rotate D right N bits		
	20	→BCD	DP	Convert S into BCD		
	21 →BIN		DP	Convert S into Binary		
	55	B→G	DP	Binary to Gray code conversion		
	56	G→B	DP	Gray code to Binary conversion		
	57	DECOD	Р	Decode the Ns ~ NL of S		
Code	58	ENCOD	P	Encode the Ns ~ NL of S		
conversion	59	→7SG	Р	Convert N+1' Nb of S into 7-segment code		
	60	→ASC	Р	Convert character/number into ASCII code		
	61	→SEC	Р	Represent hour, minute, second by seconds		
	62	→HMS	Р	Represent second by hour, minute and second		
	63	→HEX	Р	Convert ASCII code into hexadecimal		
	64	→ASCII	Р	Convert hexadecimal into ASCII code		
	0	MC		Master control loop start		
	1	MCE		Master control loop end		
	2	SKP		The start of the skip loop		
	3	SKPE		The end of the skip loop		
		END		Terminate the execution of program (for debugging)		
Flow	22	BREAK	Р	Exit from FOR-NEXT loop		
control	65	LBL		Define the string as label		
	66	JMP	P	Jump instruction		
	67	CALL	Р	Call instruction		
	68	RTS		Subroutine return instruction		
	69	RTI		Interrupt return instruction		
	70	FOR		The start of the FOR loop program		
	71	NEXT		Return point of FOR loop		
	74	IMDIO	Р	Refresh I/O immediately		
	76	TKEY	D	10 keys input convenient instruction		
I/O instruction	77	HKEY	D	16 keys input convenient instruction		
	78	DSW	D	Thumbwheel switch input convenient instruction		
	79	7SGDL	D	7-segment multiplexing display convenient Instruction		

Category	NO.	Instruction	Derivative	Function
outegory	80	MUXI	Donivative	Multiplexing input convenient instruction
	81	PLSO	D	Pulse output(PSO) instruction
				Pulse width modulation output (PWM)
I/O	82	PWM		instruction
instruction	83	SPD		Speed detection instruction
	84	TDSP		7/16-segment LED display control
	86	TPCTL		PID temperature control
	139	HSPWM		Hardware PWM pulse output
Cumulative	87	T.01S		0.01S time base cumulative timer
Timer	88	T.1S		0.1S time base cumulative timer
	89	T1S		1S time base cumulative timer
Monitor and	90	WDT	Р	Set watchdog timer
control	91	RSWDT	Р	Reset watchdog timer
HSC/	92	HSCTR		Read CV of hardware high speed counter/timer
HST	93	HSCTW		Write CV or PV of hardware high speed counter/timer
Text	94	ASCWR		Output ASCII message
Ascend/ Descend	95	RAMP		Ascending/Descending convenient instruction
	150	M-BUS		Modbus protocol communication
Communication	151	CLINK		Fatek/Generic protocol communication
	100	$R{ ightarrow}T$	DP	Move register Rs to the table Td
	101	T→R	DP	Move the Rp of table Ts to register Rd
	102	Т→Т	DP	Move the Rp of table Ts to the Rp of table Td
	103	BT_M	DP	Move table Ts to table Td
	104	T_SWP	DP	Swap Ta and Tb
	105	R-T_S	DP	Search Rs from table Ts
Table	106	T-T_C	DP	Compare table Ta and table Tb
operation	107	T_FIL	DP	Fill Rs into Td table
	108	T_SHF	DP	Shift table left or right
	109	T_ROT	DP	Rotate table left or right
	110	QUEUE	DP	First in first out (Queue) instruction
	111	STACK	DP	First in last out (Stack) instruction
	112	ВКСМР	DP	Compare Rs with zone defined by two tables
	113	SORT	DP	Sort the table
	120	MAND	P	AND two matrixes
	121	MOR	P	OR two matrixes
	122	MXOR	P	XOR two matrixes
	123	MXNR	P	XNR two matrixes
	124	MINV	P	Inverse matrix
Matrix operation	125	МСМР	Р	Compare two matrixes and find out the differences between two matrixes
•	126	MBRD	P	Read the bit of a matrix pointed by pointer
	127	MBWR	P	Write the bit of a matrix pointed by pointer
	128	MBSHF	P	Shift matrix left 1 bit or right 1 bit
	129	MBROT	Р	Rotate matrix left 1 bit or right 1 bit
	130	MBCNT	Р	Count the number of bit whose value is 1 in matrix
	140	HSPSO		Hardware NC pulse output
NC	141	MPARA		Set NC position parameters
Position control	142	PSOFF	P	Force to stop HSPSO
CONTROL	143	PSCNV	Р	Convert pulse count into mechanical value for display
Interrupt	145	EN	Р	Enable external input or peripheral interrupt/operation
control	146	DIS	Р	Disable external input or peripheral interrupt/operation



# Environmental specifications / Power supply specifications / Main unit specifications

### ■ Environmental specifications

	Item		Specification	Note
	Enclosure Minimu		5°C	
Operating an	nbient space	Maximum	40°C	Permanent installation
temperature	Open sp	Minimum	5°C	i emanent installation
	Open sp	Maximum	55°C	
Storage temp	Storage temperature		-25°C ~ +70°C	
Relative hum	Relative humidity(non-condensing, RH-2)		5% ~ 95%	
Pollution res	Pollution resistance		Degree II	
Corrosion re	sistance		Base on IEC-68 standard	
Altitude			≤2000m	
Vibration	Fixed by DIN RA	IL .	0.5G, 2 hours for each direction of 3 axes	
resistance	resistance Fasten by screw		2G, 2 hours for each direction of 3 axes	
Shock resistance			10G, Three times for each direction of 3 axes	
Noise resista	ince		1500 Vp-p, pulse width 1µS	
Withstand vo	oltage		1500VAC, 1 minute	L \ N to any terminal

### ■ Power supply specifications

• AC power supply

Item	Specification	10/14 points main unit	20/24 points main unit	32/40 points main unit	60 points main unit		
Innut rongo	voltage		100 ~ 240VAC -15%/+10%				
Input range	Frequency	50/60Hz ±5%					
Max. power consumption (built-in power supply)		21W (POW-14) 36W (POW-24)					
Inrush current	Inrush current		20A @ 264VAC				
Allowable power mometary interruption time		<20mS					
Fuse rating		1A, 250VAC					

#### • DC power supply

Specification Item	10/14 points main unit	20/24 points main unit	32/40 points main unit	60 points main unit
Input range	24VDC -15%/+20%			
Max. power consumption (built-in power supply)	15W (DPOW-10)	24W (DPOW-16)		
Inrush current	20A @ DC24 V			
Allowable power mometary interruption time	<20mS			
Fuse rating	3.15A, 250VAC			

#### ■ Main unit specifications

\* is default, user configurable

	•			
	Item	Specification	Note	
Execution speed		0.33uS/Sequential instruction in average		
Program capacity	1	20K Words		
Program memory	•	FLASH ROM or SRAM + Lithium battery for Back-up		
Sequential instru	ction	36 instructions		
Function instruct	ion	326 instructions (126 kinds) Include derivative instructions		
Flow chart comm	and (SFC)	4 instructions		
	Port0 (RS232 or USB)	Communication speed 4.8Kbps ~ 921.6Kbps (9.6Kbps)*		
Communication Interface	Port1 ~ Port4 (RS232, RS485 or Ethernet)	Communication speed 4.8Kbps ~ 921.6Kbps (9.6Kbps)*	Port1 ~ 4 provide FATEK or Modbus master/slave communication protocol	
	Maximum link stations	254		

#### Main unit specifications

(Continue) \* is default,user configurable

וווטכ	illue)								is deladit,user cornigurable			
		Ite	m			Specif	fication		Note			
	х	Input contact (DI)			X0 ~ X255 (256)				Corresponding to external digital input			
	Υ				Y0 ~ Y255 (256)				Corresponding to external digital output			
	TR				TR0 ~ TR39 (40)							
!					M0 ~ M799 (800)*				Can be configured as retentive type			
		Internal relay		Non-retentive	M1400 ~ M1911 (5	512)						
Digital (Bit status)	М			Retentive	M800 ~ M1399 (60	00)*			Can be configured as non-retentive type			
		Special relay			M1912 ~ M2001 (9	90)						
	_			Non-retentive	S0 ~ S499 (500)*				S20 ~ S499 can be configured as retentive type			
	S	Step relay		Retentive	S500 ~ S999 (500)	)*			Can be configured as non-retentive type			
	Т	Timer "Time U	Jp" stat	tus contact	T0 ~ T255 (256)							
	С	Counter "Cou	ınt Up"	status contact	C0 ~ C255 (256)							
		Timer	0.018	Time base	T0 ~ T49 (50)*							
	TMR	current	0.1S T	ime base	T50 ~ T199 (150)*				T0 ~ T255 numbers for each time base can be			
		value		ne base	T200 ~ T255 (56)*				adjusted.			
		register	13 1111						Can be configured as non-retentive type			
		Counter	16-bit	Retentive Non retentive	C0 ~ C139 (140)* C140 ~ C199 (60)*				Can be configured as retentive type			
	CTR	current value		Non-retentive Retentive	` '				Can be configured as per retentive type			
		register	32-bit		C200 ~ C239 (40)*				Can be configured as retentive type			
				Non-retentive	C240 ~ C255 (16)*				Can be configured as pen retentive type			
	HR			Retentive	R0 ~ R2999 (3000) D0 ~ D3999 (4000)		Can be configured as non-retentive type					
1	DR			Non vetentive			O b					
		Data register		Non-retentive	R3000 ~ R3839 (840)*  R5000 ~ R8071 (3072)*				Can be configured as retentive type			
	HR			Retentive					When not configured as ROR,it can serve normal register (for read/write)			
	ROR			Read only register		n be set as ROR ~ o	default setting is (0)	*	ROR is stored in special ROR area and not consume program space			
		File register			F0 ~ F8191 (8192)				Must save/retrieved via special commands			
	IR	Input register			R3840 ~ R3903 (6				Corresponding to external numeric input			
	OR	Output registe			R3904 ~ R3967 (6	•			Corresponding to external numeric output			
	SR	Special system register				97), R4000 ~ R4095	5 (96)		Except R4152 ~ R4154			
	(Special register)	0.1mS high-speed ti		-	R4152 ~ R4154 (3)							
	cial	High-speed Counter register		Hardware (4 sets)	DR4096 ~ DR4110							
	reç			Software (4 sets)	DR4112 ~ DR4126 (4x4)							
	iste	Calendar regi	ster		R4128 (sec)	R4129 (min)	R4130 (hour)	R4131 (day)	Not available in MA model			
					R4132 (month)	R4133 (year)	R4134 (week)					
	XR	Index register			V, Z (2), P0 ~ P9 (1							
	rupt	External inter				oints input positive/r						
	rol	Internal interr		ntrol		3, 4, 5, 10, 50, 100m						
lm	S high s	speed timer(HS				its, share with HHS0	C)					
	Hardwa	are high-speed		No. of channel	Up to 4							
		er (HHSC) /32bits		(HHSC) /32bits Counting mode			0x2, K/R, K/Rx2, A/E	Total number of HHSC and SHSC is 8				
				Counting frequency	Maximum is 120KH Up to 4	Hz (Single end input	ential input)	HHSC can be converted into 32 bits/0.1mS tim				
	Softwa	re high-speed	high-speed			A (D)		base high-speed timer				
		r (HHSC) /32bi	ts	Counting mode	3 modes (U/D, K/R							
		N		Counting frequency	Maximum sum up t	to 10KHz						
;		Number of a			Up to 4				11.15.61			
	tion	Output frequ				Hz (Single end outpo	ut) or 920KHz (diffe	erential output)	Half of the maximum while A/B output			
Ise	e out	Pulse out mo			3 modes (U/D,K/R,							
SP	SO)	Programmin	_	oa	Dedicated position							
		Interpolation			Maximum 4 axes li	near interpolation						
	WM	Number of p	oints		Up to 4	(with 0.49/						
tp	ut	Output frequ	iency			z (with 0.1% resolut dz (with 1% resoluti						
			P	oints	Up to 36							
					>10 µS (High spee	ed)						
apt	ured inp	out	P	ulse width	>47 µS (Medium s							
					>470 µS (Medium							
						frequency 14KHz ~	1.8MHz		Chosen by frequency at high frequency			
git	al filter		X	0 ~ X15		nstant 0 ~ 1.5mS/0-		ImS)	Chosen by time constant at low frequency			
						S ~ 15mS, adjustabl						

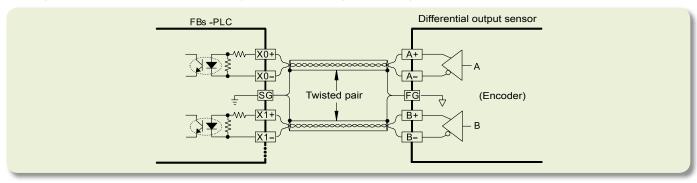


#### ■ Digital input (DI) specifications

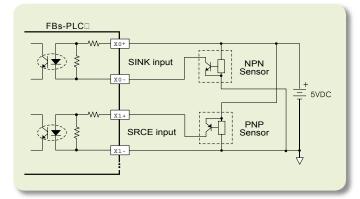
	Specification	5VDC differential input		24VDC single	e-end input		
Item		Ultra high speed 920KHz	High speed 120KHz	Medium speed (HSC) 20KHz*1	Medium low Speed (Captured) 470µS*²	Low speed 4.7mS	Note
Input signal voltage		5VDC ±	10%				
Threshold	ON	> 6mA	> 4mA	> 4mA		> 2.3mA	
current	OFF	< 2mA	< 1.5mA	< 1.5mA		< 0.9mA	*1 Limit of input speed in MA
Maximum inpu	ut current	20mA	7mA	7mA		4.2mA	model is 10KHz
Input indication	n		*2 For captured inputs				
Isolation meth	od						
SINK/SRCE wiring		Independent wiring	Via variation of	internal common termi			
Noise filtering	methods	DHF (0nS ~ +AHF (47	. ΔHF (4.7mS)				DHF: Digital hardware filter AHF: Analog hardware filter

Note: In this catalog, All the In/Out type of "Source" is denoted by its abbreviation - "SRCE"

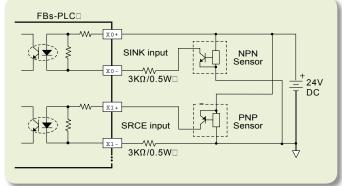
• Wiring of 5VDC differential input (with frequency up to 920KHz for high speed or high noise environments)



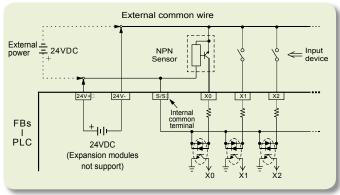
Wiring of 5VDC differential input to 5VDC single-end SINK /SRCE input (Max. 120KHz)



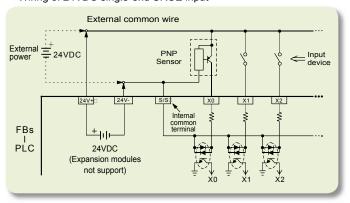
Wiring of 5VDC differential input to 24VDC single-end SINK /SRCE input (Max. 120KHz)



Wiring of 24VDC single-end SINK input



Wiring of 24VDC single-end SRCE input

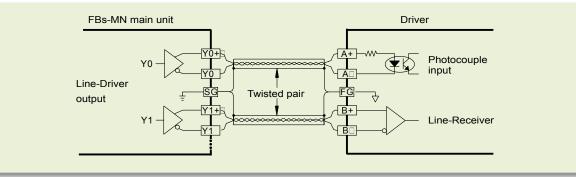


## ■ Digital output (DO) specifications

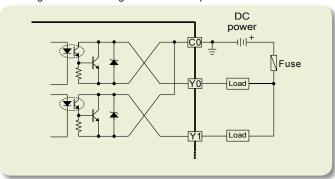
	Specification	Differential output	Singl	e-end transistor o	Single-end	Single-end				
Item		Ultra high speed	High speed	Medium speed	Low speed	relay output	TRIAC output			
Maximum swicthing (v	vorking) frequency	920KHz*	120KHz*	20KHz*	200Hz	For ON/OFF, not suitable for switching frequently				
Working voltage		5VDC		5 ~ 30 VDC		< 250VAC, 30VDC	100 ~ 240VAC			
Maximum load	Resistive		0.5A	0.2A	0.5A 0.1A (24EYT)	2A/single, 4 A/common	1A			
current	Inductive	50mA				80VA	15VA/100VAC 30VA/200VAC			
Maximum voltage drop (@ maximum load)	5	-	0.6V	2.2V 1.2V		0.06V (initial)	1.2Vrms			
Minimum load		-		-		2mA/DC power	25mA			
Leakage current		-	< 0.1mA/30VDC		-	2mA				
Maximum output	$\text{ON} \rightarrow \text{OFF}$	200nS	200nS	15µS	1mS	10mS	1mS			
delay time	$\text{OFF} \to \text{ON}$	200110	200110	30µS	iiio	101110	1/2AC cycle			
Output status indication	on	Displayed by LED: Lit when "ON", dark when "OFF"								
Over current protectio	n	N/A								
Isolation type			Photocouple	isolation	Electromagnetic isolation	Photocouple isolation				
SINK/SRCE output typ	e	Independent dual terminals for arbitrary choose SINK/SRCE by models and non-exchangeable connection  Bilateral device, can be arbitrarily set to SINK/S								

<sup>\*:</sup> Half of the maximum while A/B output

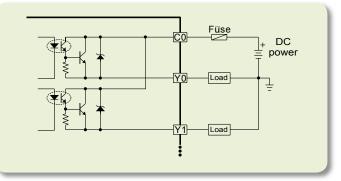
• Wiring of 5VDC differential output (Up to 920KHz for U/D/CK output; Up to 460KHz for A/B output; For high speed or high noise environments)



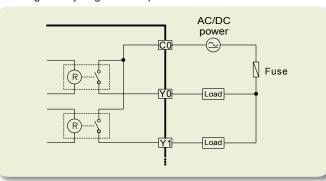
• Wiring of transistor single-end SINK output



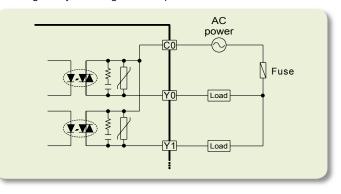
• Wiring of transistor single-end SRCE output



· Wiring of relay single-end output



Wiring of Thyristor single-end output





#### NC positionning main units (MN) / High-performance main units (MC)

#### NC positionning main units (MN)

(7.62 mm detachable terminal block)



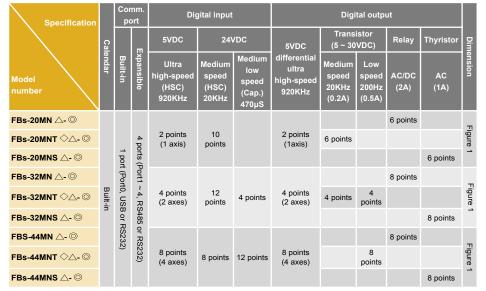
FBs-20MN (T,S)



FBs-32MN (T,S)



FBs-44MN (T,S)



- ∴ Port0 interface: Blank—RS232, U—USB
- ⇒: Transistor output type: Blank—SINK output (NPN), J—SRCE output (PNP)
- ©: Power supply: Blank—AC power supply (100 ~ 240VAC), D—DC power supply (24VDC)

## High-performance main units (MC)

(7.62 mm detachable terminal block)



FBs-10MC (T,S)



FBs-14MC (T,S)



FBs-20MC (T,S)



FBs-24MC (T,S)



FBs-32MC (T,S)



FBs-40MC (T,S)



FBs-60MC (T,S)

Specification		Comm. port		Digital input			Digital output													
Opecinication	ဂ္ဂ			24VDC			Transistor (5 ~ 30VDC)			Relay	Thyristor	말								
Model number	Calendar	Built-in	Expansible	High speed (HSC) 120KHz	Medium speed (HSC) 20KHz	Medium low speed (Cap.) 470µS	High speed 120KHz (0.5A)	Medium speed 20KHz (0.2A)	Low speed 200Hz (0.5A)	AC/DC (2A)	AC (1A)	Dimension								
FBs-10MC △- ◎-X										4 points		<u> </u>								
FBs-10MCT ◇△-◎-XY					4 points		2*~4 points	2 points				Figure								
FBs-10MCS △- ◎-X				2*~4							4 points	2								
FBs-14MC △- ◎-X				points	•					6 points		Ţ								
FBs-14MCT △- ◎-XY					6 points		2*~6 points	4 points				Figure								
FBs-14MCS △- ◎-X											6 points	2								
FBs-20MC △- ◎-X			4	2*~6	10					8 points		Ţ								
FBs-20MCT ◇△-◎-XY		1 p	ports (Port1 ~ 4, RS485 or RS232)	points	points		2*~8 points	6 points				Figure								
FBs-20MCS △- ©-X		1 port (Port0, USB or RS232)	(Por								8 points									
FBs-24MC △- ◎-X	В	ort0,	1 ~		12					10 points		Ţ								
FBs-24MCT ◇△-◎-XY	Built-in	USE	4, RS		points		2*~8 points	6 points	2 points			Figure								
FBs-24MCS △- ◎-X	_	3 or F	3485								10 points	_								
FBs-32MC △- ◎-X		RS23:	or Ro							12 points		Ţ								
FBs-32MCT ◇△-◎-XY		2)	3232)			4 points	2*~8 points	6 points	4 points			Figure								
FBs-32MCS △- ◎-X				2*~8							12 points									
FBs-40MC △- ◎-X				points	44					16 points		Ţ								
FBs-40MCT ◇△-◎-XY					14 points	8 points	2*~8 points	6 points	8 points			Figure								
FBs-40MCS △- ◎-X											16 points	_								
FBs-60MC △- ◎-X						00				24 points		<u>_</u>								
FBs-60MCT ◇△-◎-XY						20 points	2*~8 points	6 points	16 points			Figure								
FBs-60MCS △- ◎-X														F 2					24 points	

△ : Port0 interface : Blank—RS232, U—USB

- X: Extra high speed input (120KHz) points
- ∴ Transistor output type : Blank—SINK output (NPN), J—SRCE output (PNP)

   Y: Over supply: Blank—AC power supply (100 ~ 240VAC), D—DC power supply (24VDC)
  - Y: Extra high speed output (120KHz) points

<sup>\*:</sup> The standard MC main units has the built-in 2 points of high speed input and 2 points of high speed output. For optional order, can be 1 ~ 6 points of high speed I/O expandable. I/O expanding points to be specified at column X (input number) and Y(output number) of model number. For example FBs-40MCT-21 means extra 2 points of high speed input (total 4 points) and one more point of high speed output (total 3 points). Another example FBs-24MCT-03 means only 3 more points of high speed output (total 4

#### Economical main units (MA) / Digital expansion units

#### **■ Economical main units** (MA)

(7.62 mm terminal block)



FBs-10MA (T,S)



FBs-14MA (T,S)



FBs-20MA (T,S) FBs-24MA (T,S)





FBs-32MA (T,S)

FBs-40MA (T,S)



FBs-60MA (T,S)

Specification		Comm. port		Digital input		Digital output				
Opecinication	Cale		m	24VDC		Transistor (5 ~ 30VDC)		Relay	Thyristor	Dime
Model number	Calendar	Built-in	Expansible	Medium Speed 10KHz	Medium Iow (Cap.) 470µS	Medium speed 10KHz (0.2A)	Low speed 200Hz (0.5A)	AC/DC (2A)	AC (1A)	Dimension
FBs-10MA △- ◎								4 points		ī
FBs-10MAT ♦△- ◎					2 points	4 points				Figure
FBs-10MAS △- ◎									4 points	2
FBs-14MA △- ◎								6 points		Ţ
FBs-14MAT ◇△- ◎					4 points	4 points	2 points			Figure
FBs-14MAS △- ◎									6 points	2
FBs-20MA △- ◎			2					8 points		<u></u>
FBs-20MAT ◇△- ◎		1 pc	ports (Port1~2, RS485 or RS232)		8 points	4 points	4 points			Figure 1
FBs-20MAS △- ◎		ort (F	(Po						8 points	_
FBs-24MA △- ◎		orto	T1~					10 points		Ţ
FBs-24MAT ◇△- ◎	Z o	, US	2, RS	4 points	10 points	4 points	6 points			Figure
FBs-24MAS △- ◎		B or	3485						10 points	_
FBs-32MA △- ◎		1 port (Port0, USB or RS232)	or F					12 points		Ţ
FBs-32MAT ◇△- ◎		32)	<b>RS23</b>		16 points	4 points	8 points			Figure
FBs-32MAS △- ◎			2)						12 points	_
FBs-40MA △- ◎					00			16 points		Ţ
FBs-40MAT ◇△- ◎					20 points	4 points	12 points			Figure
FBs-40MAS △- ◎					p =				16 points	_
FBs-60MA △- ◎								24 points		Figure
FBs-60MAT ◇△- ◎					32 points	4 points	20 points			
FBs-60MAS △- ◎									24 points	_

- ∴ Port0 interface: Blank—RS232, U—USB
- ⇒: Transistor output type: Blank—SINK output (NPN), J—SRCE output (PNP)
- $\odot$ : Power supply: Blank—AC power supply (100 ~ 240 VAC), D—DC power supply (24 VDC)

## **■ Digital expansion units**

(7.62 mm terminal block)



FBs-24EAP(T,S)



FBs-40EAP(T,S)



FBs-60EAP(T,S)

	Digital input	Dig			
Specification	24VDC	Transistor (5 ~ 30VDC)	Relay	Thyristor	Dim
Model number	Low speed 4.7mS	Low speed 200Hz (0.5A)	AC/DC (2A)	AC (1A)	Dimension
FBs-24EAP- ◎			10 points		22
FBs-24EAPT △- ◎	14 points	10 points			Figure
FBs-24EAPS- ©	P			10 points	_
FBs-40EAP- ©			16 points		22
FBs-40EAPT △- ◎	24 points	16 points			Figure
FBs-40EAPS- ©	F			16 points	7
FBs-60EAP- ◎			24 points		23
FBs-60EAPT △- ◎	36 points	24 points			Figure
FBs-60EAPS- ©	F-#10			24 points	7

- $\triangle$ : Transistor output type: Blank—SINK output (NPN), J—SRCE output (PNP)
- ♦: Power supply: Blank—AC power supply (100 ~ 240 VAC), D—DC power supply (24 VDC)